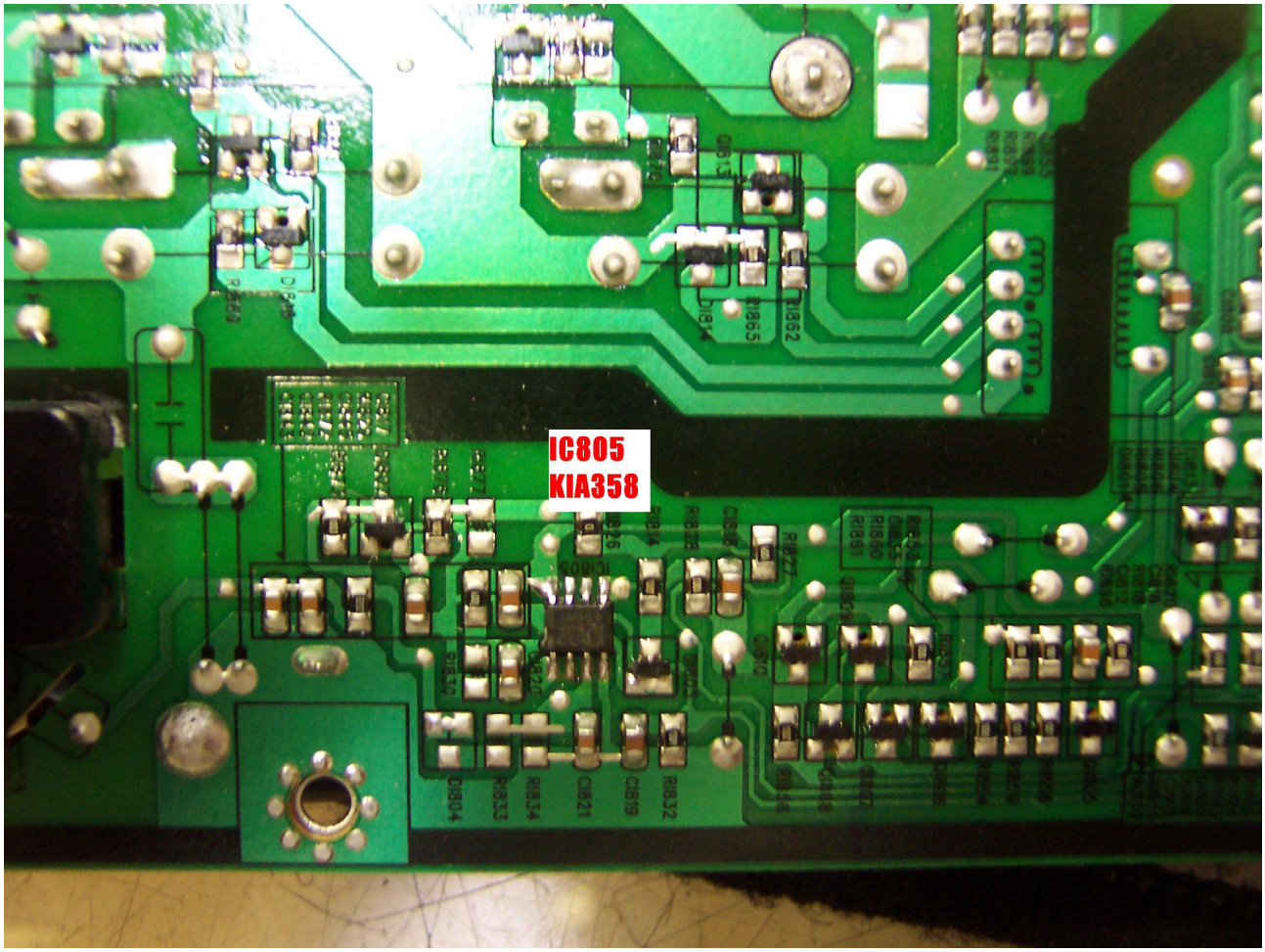
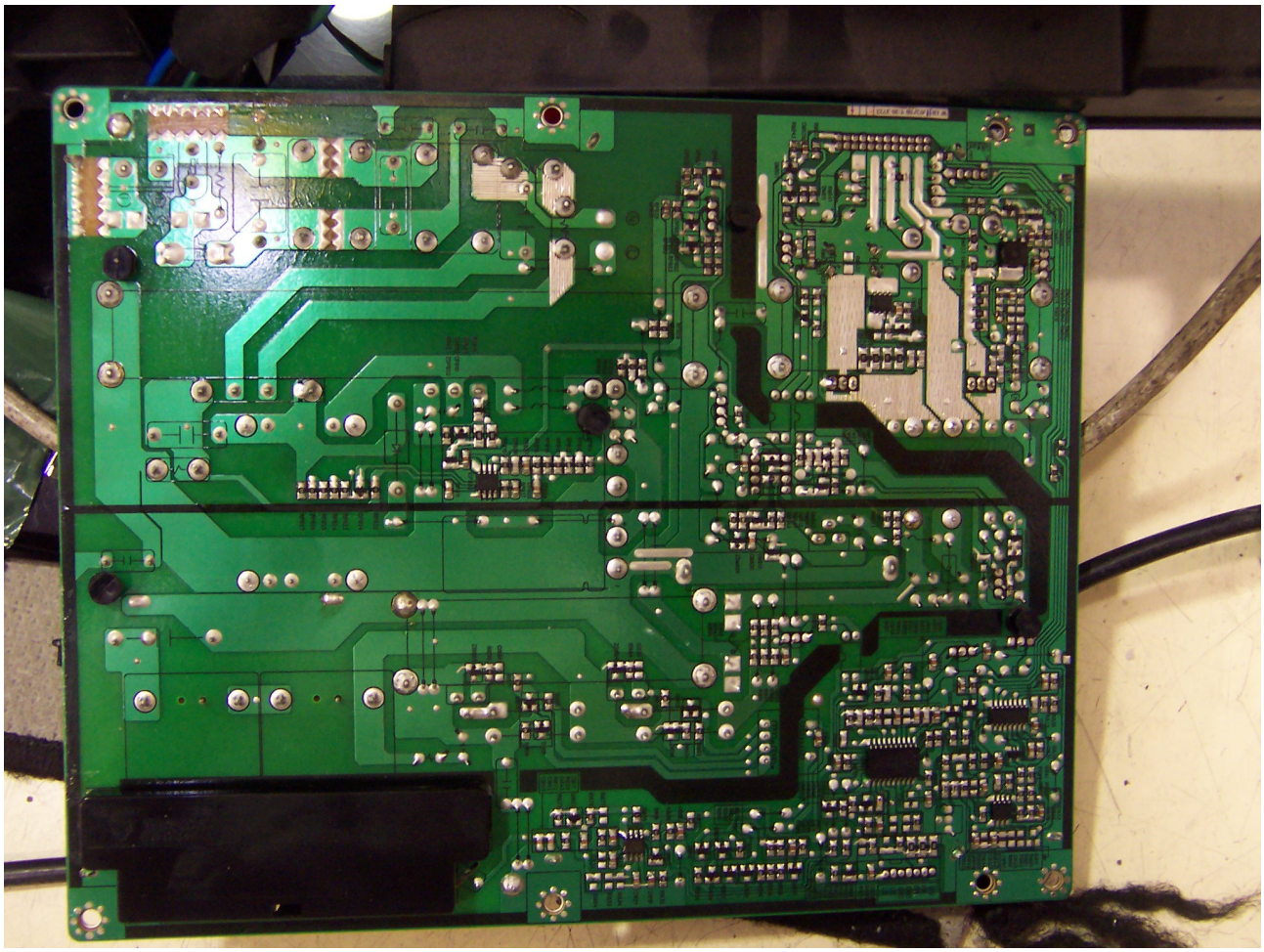
TV SAMSUNG LE40A656AF1

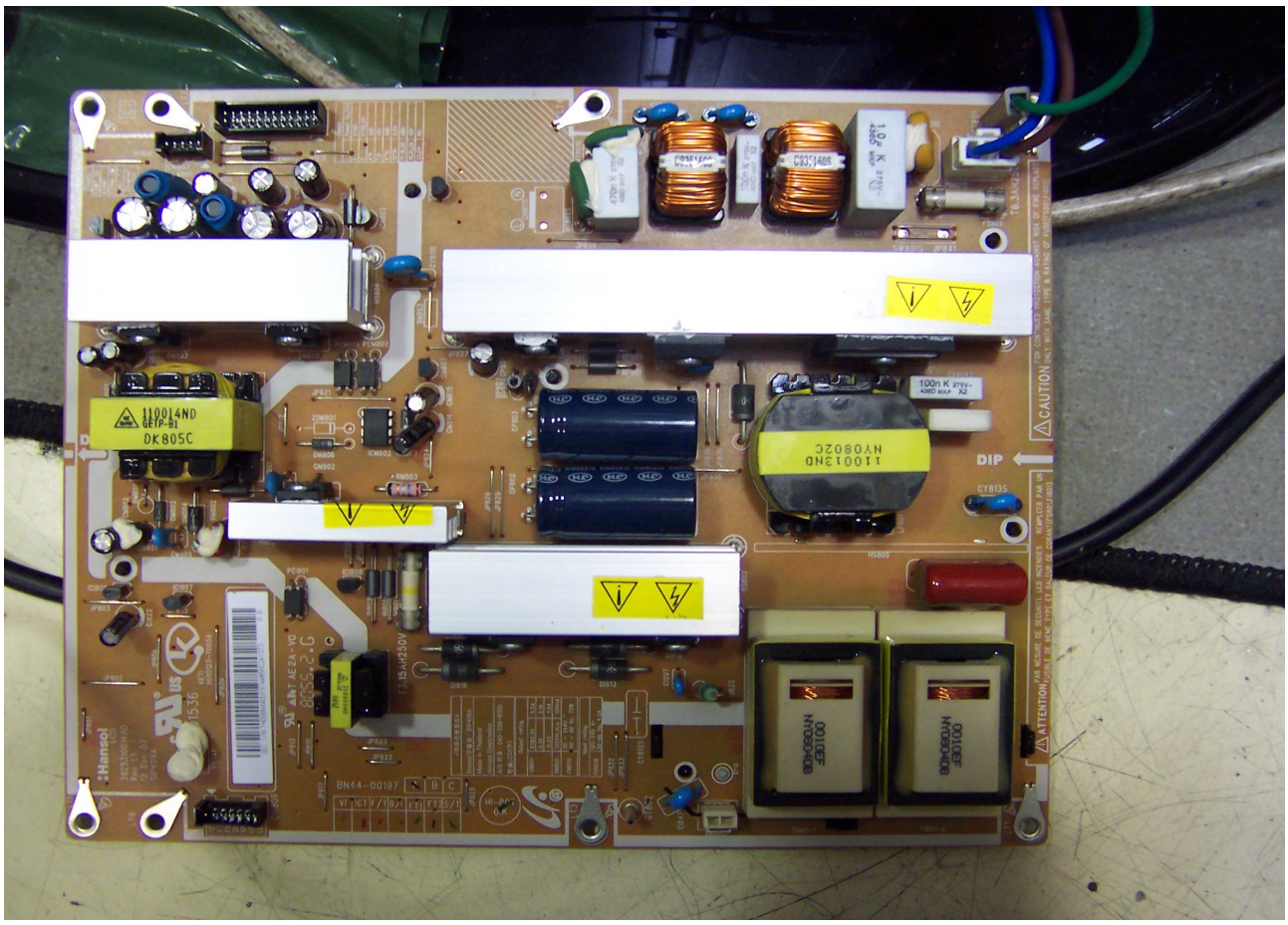
SÍNTOMA:AL POCO DE ENCENDER SE APAGA PANTALLA Y QUEDA SONIDO

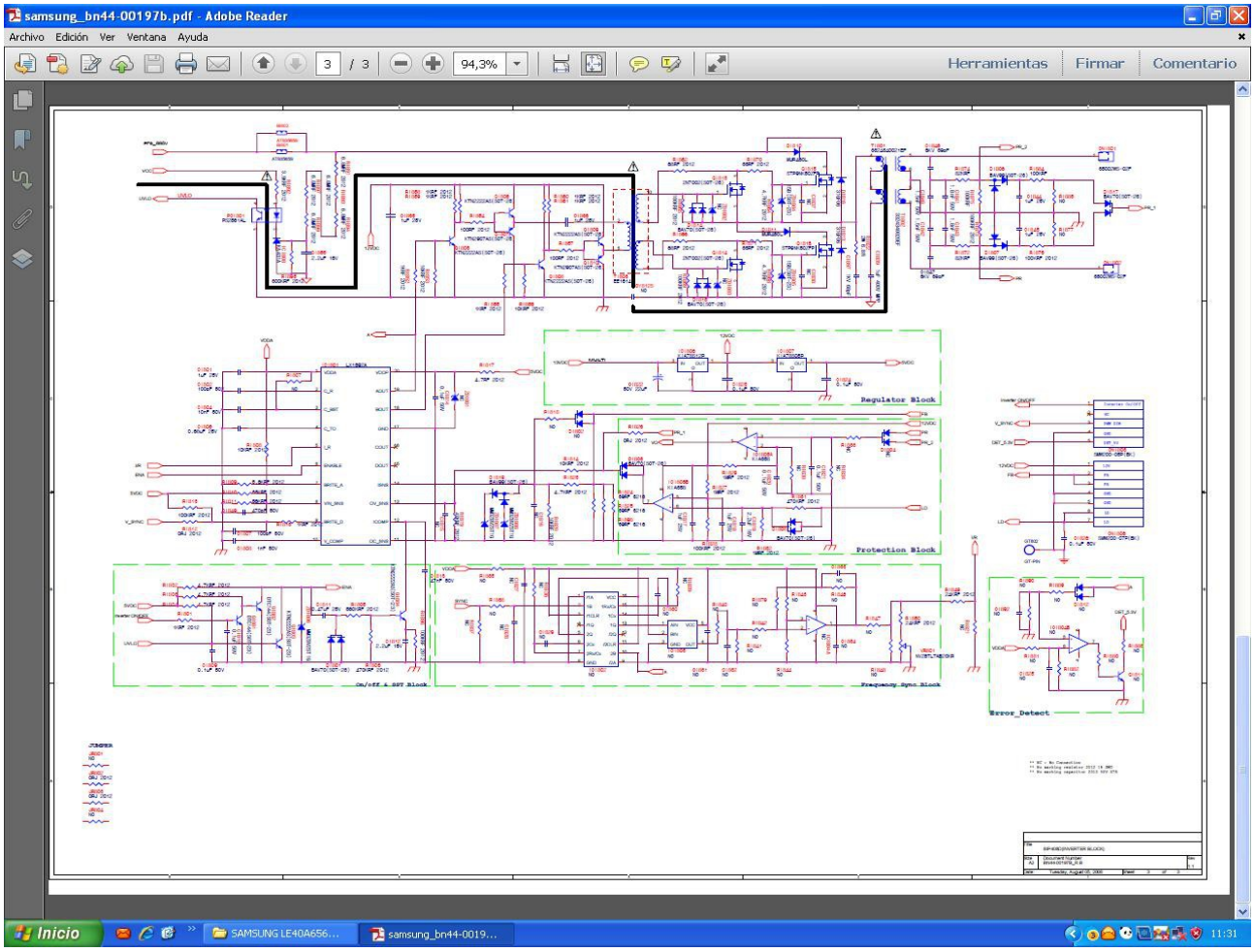
REPARACIÓN
AVERÍA EN PLACA DE ALIMENTACIÓN-INVERTER BN44-00197A
CIRCUITO INVERTER BASADO EN EL IC LX1692BIW
SOSPECHO DE UN FALLO EN LOS CIRCUITOS SENSORES DE ERRORES DE LÁMPARA Y TRABAJO EN ESA LÍNEA
BUSCO EL DATASHEET DEL IC LX1692BIW PARA VER EN QUE PINES ESTÁN LAS PROTECCIONES.INTENTO DESCUBRIR CÓMO ANULAR LA PROTECCIÓN DEL APAGADO DE LÁMPARA Y TRAS VARIAS PRUEBAS LO DESCUBRO.
PARA ELLO HAY QUE PONER EL PIN 13 DENOMINADO OV-SNS A MASA Y ASI NO SE APAGA
PERO QUIERO DESCUBRIR LA CAUSA DE LA AVERÍA PARA REPARALO DE FORMA CORRECTA
CON EL ESQUEMA DE LA FUENTE LO ESTUDIO Y VEO QUE EL IC805 KIA358,UN DOBLE OPERACIONAL,INTERVIENE EN EL CIRCUITO DETECTOR DE ERROR.
LO CAMBIO POR UN LM393 QUE ES EQUIVALENTE Y AFORTUNADAMENTE LA AVERÍA QUEDA SOLUCIONADA,YA NO SE APAGA.

A CONTINUACIÓN ADJUNTO FOTOS


IC805
KIA358




DIP
CAUTION
ATTENTION
BN44-00197
DK805C


samsung_bn44-00197b.pdf - Adobe Reader
Archivo Edición Ver Ventana Ayuda
3 / 3
94,3%
Herramientas
Firmar
Comentario
Regulator Block
Protection Block
Frequency Sync Block
Error Detect
Inicio
SAMSUNG LE40A656...
samsung_bn44-0019...
11:31

lx1692.pdf - Adobe Reader

Archivo Edición Ver Ventana Ayuda

2 / 15 130% Herramientas Firmar Comentario

Microsemi™

LX1692

Full Bridge Resonant CCFL Controller

PRODUCTION DATA SHEET

ABSOLUTE MAXIMUM RATINGS

Supply Input Voltage(VDDP)..... 6.6V
VIN_SNS .....-0.3V to (VDDP+0.5V), not to exceed +6.6V
Digital Input (ENABLE) ..... -0.3V to (VDDP+0.5V) , not to exceed +6.6V
Analog Inputs (ISNS, OV_SNS, OC_SNS)clamped to ±14V Max Peak Current ±100mA
Analog Inputs (BRITE_A, BRITE_D) ..... -0.3V to (VDDP +0.5V) , not to exceed +6.6V
Digital Outputs (AOUT, BOUT, COUT, DOUT) -0.3V to (VDDP +0.5V) , not to exceed +6.6V
Analog Outputs (I_R, ICOMP, VCOMP)..... -0.3V to (VDDP + 0.5V) , not to exceed +6.6V
Maximum Operating Junction Temperature .....150°C
Storage Temperature Range.....-65 to 150°C
Peak Package Solder Reflow Temp.(40 seconds max. exposure) .....260°C(+0, -5)

Note: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

THERMAL DATA

DW Plastic SOIC 20-Pin

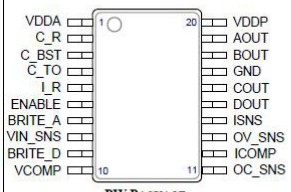
THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	85°C/W

PW Plastic TSSOP 20-Pin

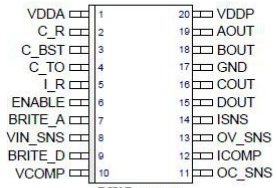
THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	99°C/W

Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$.
The θ_{JA} numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow.

PACKAGE PIN OUT



PW PACKAGE
(Top View)



DW PACKAGE
(Top View)

RoHS / Pb-free 100% Matte Tin Lead Finish

FUNCTIONAL PIN DESCRIPTION

Name	Description
C_R	Lamp Frequency Programming Capacitor Pin – lamp running frequency is set by the combination of C_R and I_R. The internal lamp current oscillator frequency can be forced to follow an external clock signal at this pin. In

www.Microsemi.com

Inicio SAMSUNG LE40A656... samsung_bn44-0019... lx1692.pdf - Adobe R... 11:34

VIDEOSON
JOSÉ DANIEL MANZANO RUIZ